

09/522026

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257	67
Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

**6483183**

6483183

U.S. **UTILITY** Patent Application

**O.I.P.E.**

SCANNED

**Q.A.**

PATENT DATE

NOV 19 2002

APPLICATION NO. 09/522026	CONT/PRIOR D	CLASS 257	SUBCLASS 600	ART UNIT 2822	EXAMINER Potter CR
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Scott Ellison

Integrated circuit (IC) package with a microcontroller having an n-bit bus and up to n-pins coupled to the microcontroller

PTO-2040  
10/00

## ISSUING CLASSIFICATION

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10/11/02 Formal Drawings (      shts) set

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 1	Figs. Drwg. 1	Print Fig. 1	Total Claims 4	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  6/17/02	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  _____ (Primary Examiner) (Date)			<b>ISSUE FEE</b> Amount Due: 280 Date Paid: 7-1-02	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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Form **PTO-436A**  
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**ISSUE FREE IN FILE**

Formal Drawings (\_\_\_\_ sets) 98

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